



Docket No.: 1076.1094

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Takashi KURIHARA, et al.

Serial No. 10/823,649

Group Art Unit: 2825

Confirmation No. 4917

Filed: April 14, 2004

Examiner: Nghia M Doan

For: METHOD AND APPARATUS FOR DESIGNING SEMICONDUCTOR INTEGRATED  
CIRCUIT

**AMENDMENT**

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Sir:

This is in response to the Office Action mailed July 28, 2006, and having a period for response set to expire on October 28, 2006. A Petition for a one-month extension of time, together with the requisite fee for same, is submitted herewith, thereby extending the period for response to November 28, 2006.

The following amendments and remarks are respectfully submitted. Reconsideration of the claims is respectfully requested.

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